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(54) **DIGITAL SEISMIC SENSOR AND ACQUISITION DEVICE ADAPTED TO BE CONNECTED TOGETHER VIA A TWO-CONDUCTOR LINE**

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(58) **Field of Classification Search**
CPC G01V 1/22; G01V 1/3808
See application file for complete search history.

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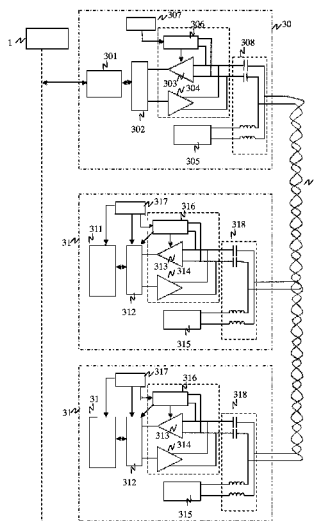
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(57) **ABSTRACT**

A digital seismic sensor adapted to be connected, via a two-conductor line, to an acquisition device. The digital seismic sensor includes: a digital sensor; a local sampling clock providing a sampling frequency; a receiver for receiving command data coming from the acquisition device and synchronization information providing accurate timing information to enable seismic sensor synchronization; a compensator for compensating, as a function of the synchronization information, a drift of the local sampling clock; a transmitter for transmitting seismic data towards the acquisition device; a driver for driving the receiver and the transmitter, according to a half-duplex transmission protocol over the two-conductor line and using a transmission clock extracted from the received command data; a power receiver for receiving electrical power; and a coupler for coupling the command and synchronization information receiver, the transmitter and the power receiver to the two-conductor line.

13 Claims, 6 Drawing Sheets



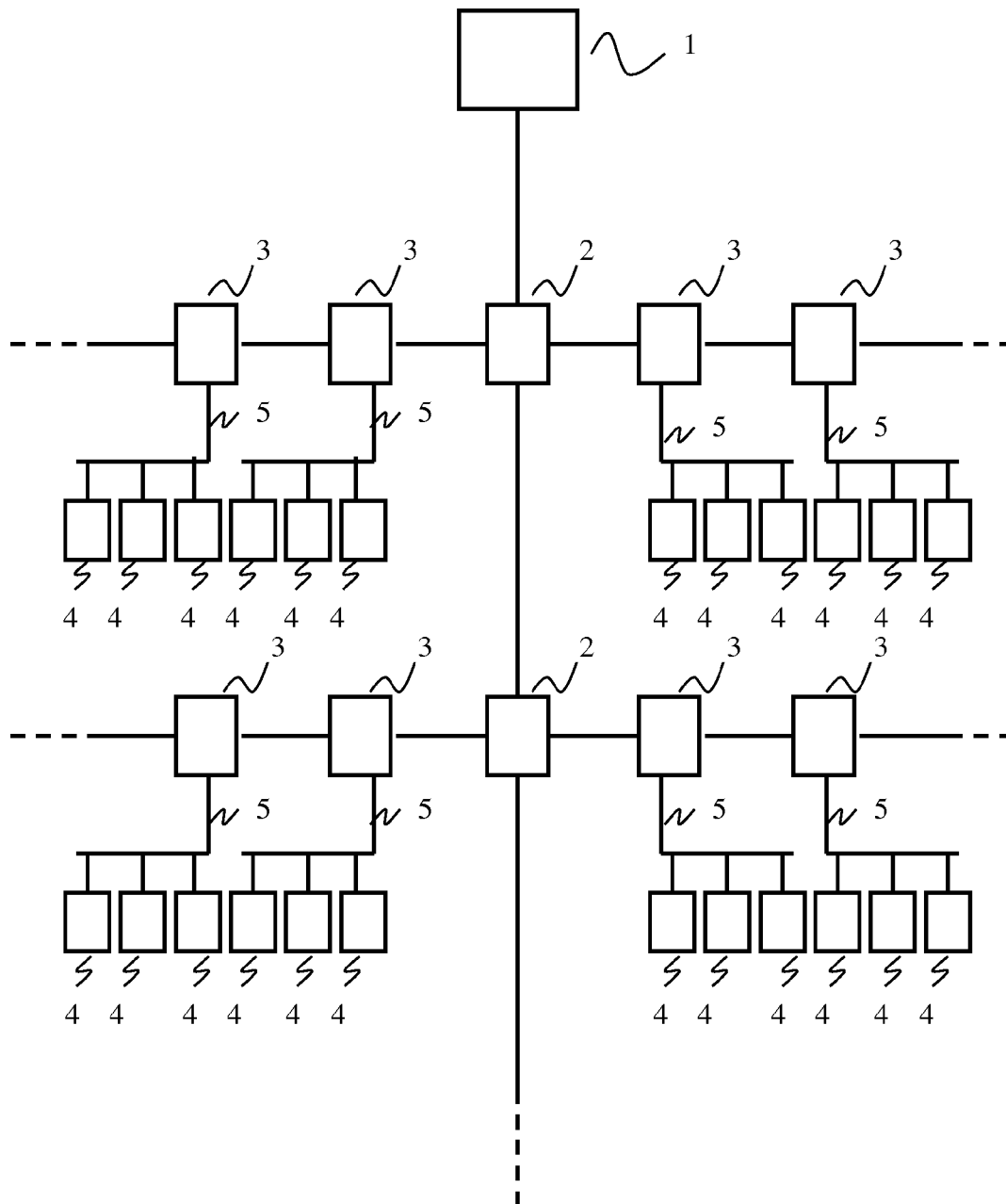


Figure 1
(prior art)

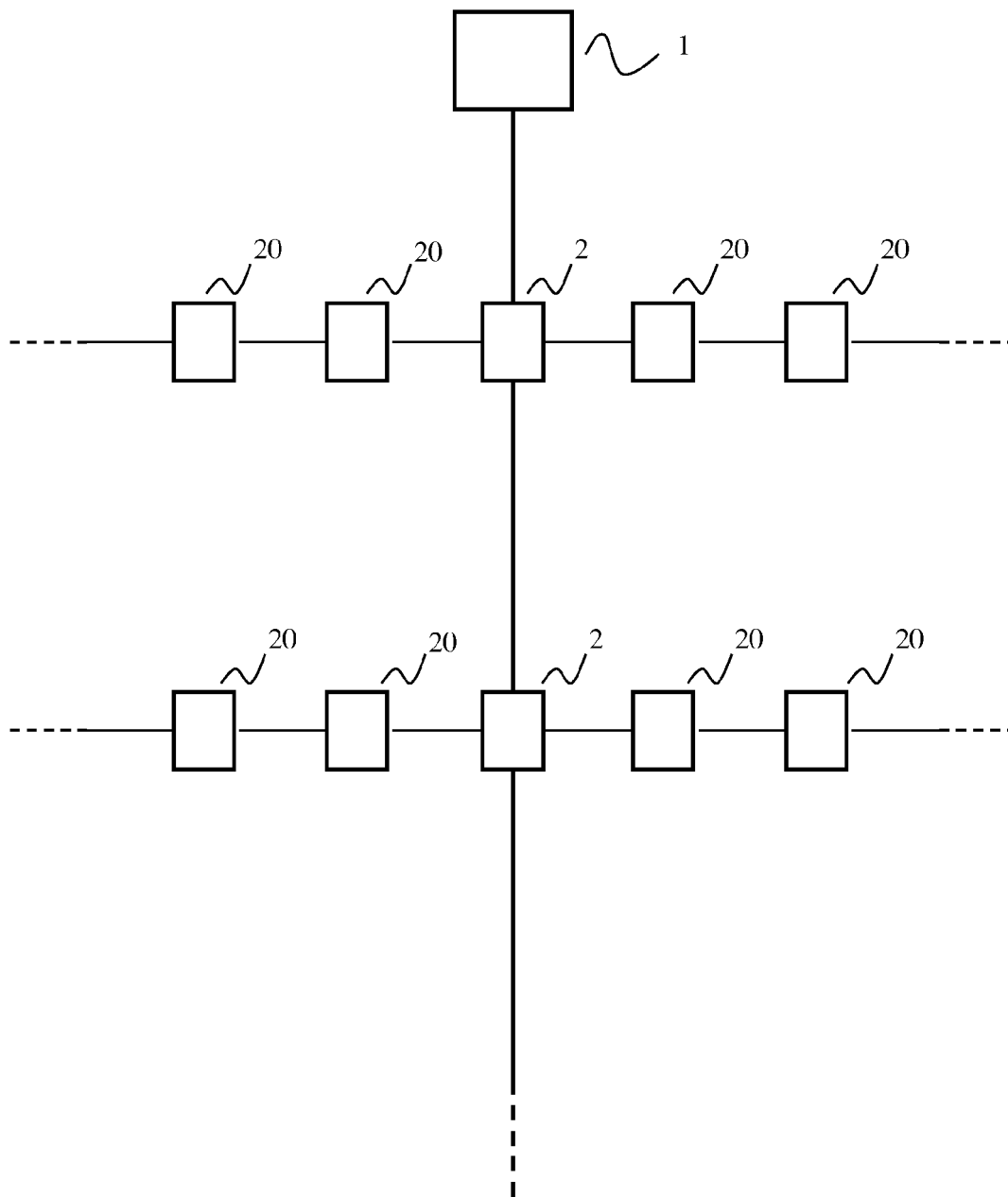


Figure 2
(prior art)

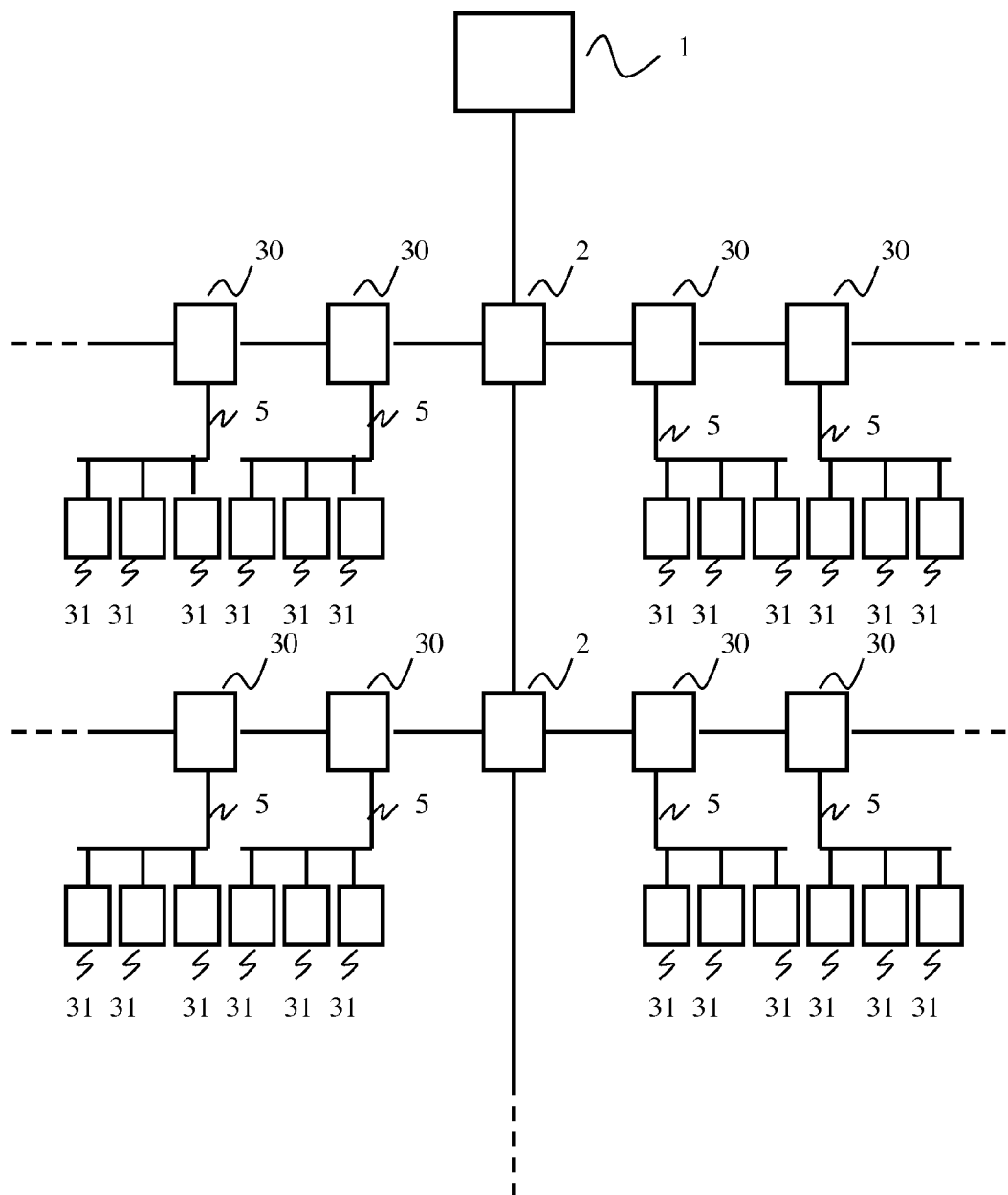
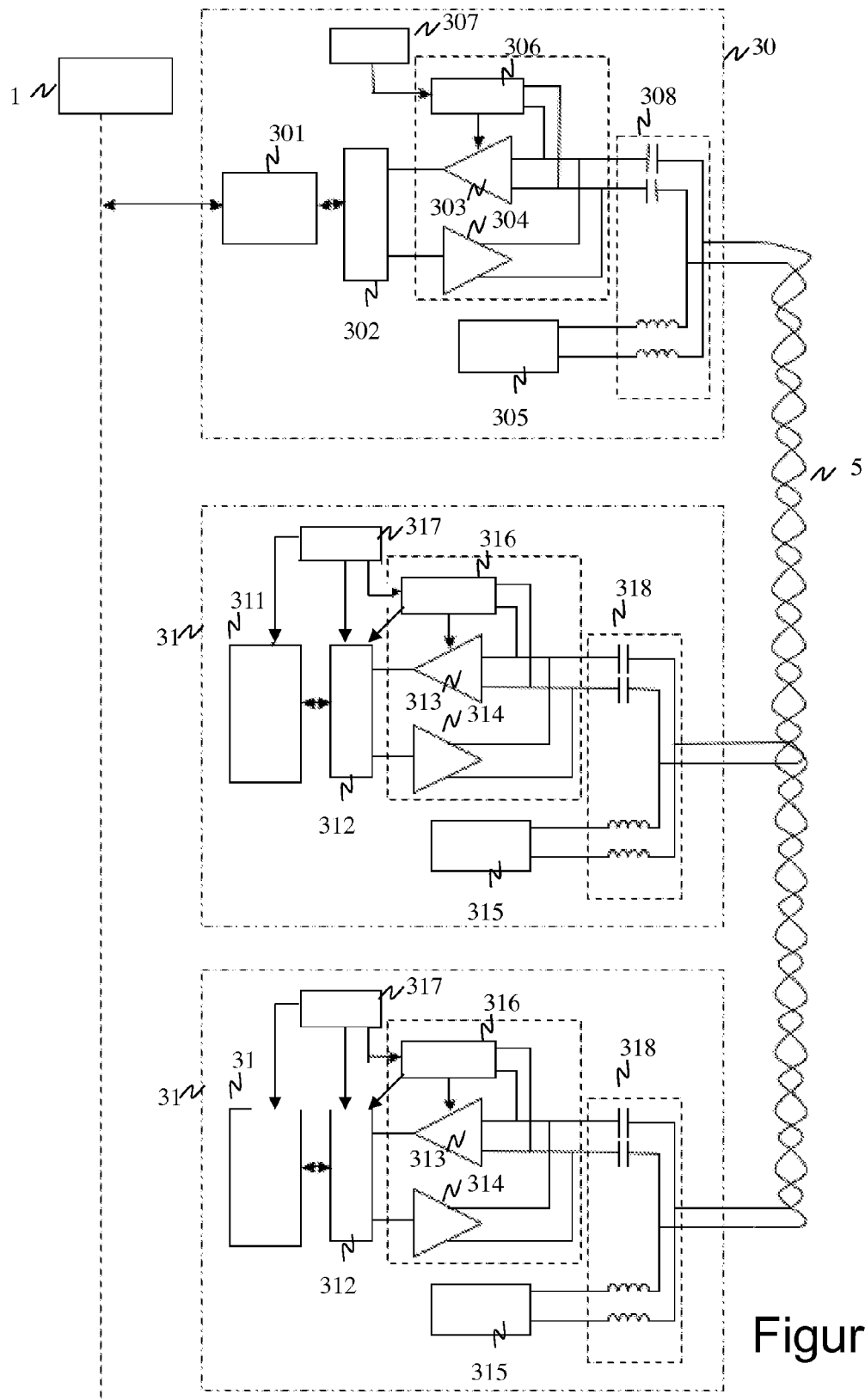


Figure 3



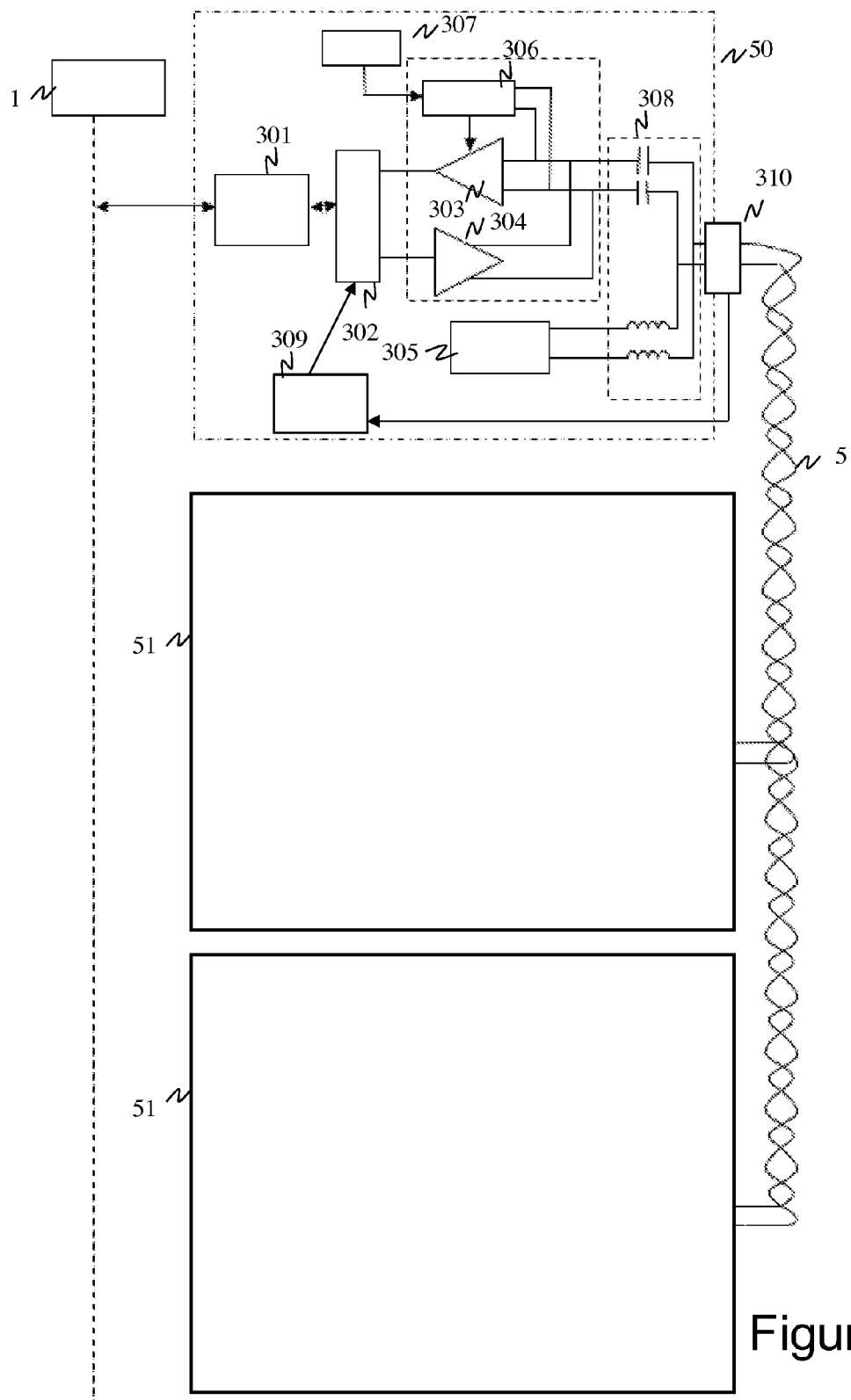


Figure 5

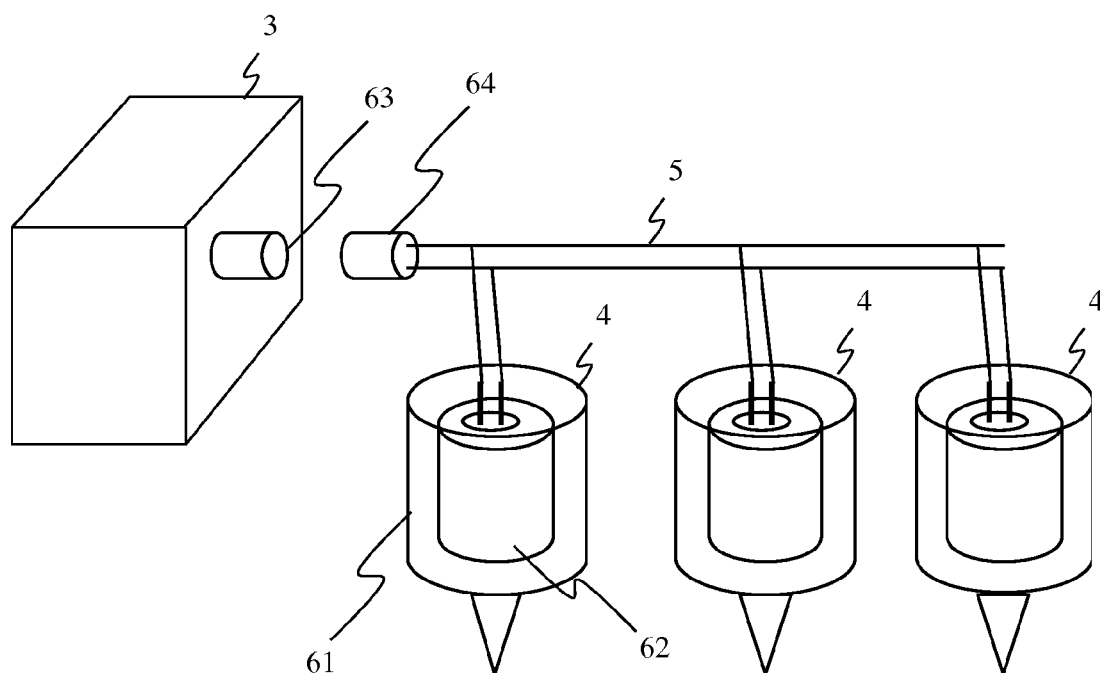


Figure 6
(prior art)

1

DIGITAL SEISMIC SENSOR AND ACQUISITION DEVICE ADAPTED TO BE CONNECTED TOGETHER VIA A TWO-CONDUCTOR LINE

1. CROSS-REFERENCE TO RELATED APPLICATIONS

None.

2. STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT

None.

3. THE NAMES OF PARTIES TO A JOINT RESEARCH AGREEMENT

None.

4. FIELD OF THE INVENTION

The field of the invention is that of seismic data acquisition. More specifically, particular embodiments of the invention relate to a digital seismic sensor and an acquisition device adapted to be connected together via a two-conductor line. A particular application of the invention relates to land seismic data acquisition systems.

These embodiments of the invention can be applied in particular in the industry of oil exploration by seismic method, but may be applied to any field implementing a seismic data acquisition.

5. TECHNOLOGICAL BACKGROUND

Seismic data acquisition systems conventionally use cabled networks comprising electronic units whereto ground movement sensors are connected.

FIG. 1 illustrates schematically a seismic data acquisition system according to a first known solution, based on the use of analog sensors 4.

For the sake of simplification, each reference 4 designates an analog sensor and its corresponding housing and casing (as detailed below with FIG. 6).

To collect the seismic data (geophysical data), one or a plurality of seismic sources (not shown in FIG. 1) in contact with the ground are activated to propagate omni-directional seismic wave trains. The sources may among other things consist of explosives, falling weights, vibrators or air guns in marine environments. The wave trains reflected by the layers of the subsurface are detected by the analog sensors 4, which generate an analog signal characterising the reflection of the waves on the geological interfaces of the subsurface.

The analog sensors 4 are generally referred to using the term "analog geophones". As shown in FIG. 6, they are generally interconnected in groups of sensors by a two-conductor line 5 (or a three-conductor line for a serial-parallel configuration) to form clusters referred to as "strings of analog geophones". To this end, each analog geophone is mounted in a mechanical housing (or cartridge) 62. This mechanical housing 62 of analog sensor is inserted with mechanical tolerances inside a casing 61 (made of plastic in general) which shape is dependent of the type of area to investigate (marsh, land . . .). The two-conductor line 5 is usually moulded to the casing 61.

Each of the strings is connected to an acquisition device 3 (several strings can be connected to the same acquisition device). To this end, the acquisition device is also mounted in

2

a mechanical housing which comprises a connector 63 with two contacts, adapted to cooperate with a connector 64 of the same type placed on the two-conductor line 5 (i.e. the cable of the string).

A string of analog geophones allows to filter the noise (spatial filtering), since the analog information circulating on the two-conductor line 5 (towards the acquisition device 3) is the average of measurements made by each of the analog geophones.

The acquisition devices 3 are generally referred to using the term "Digitizer Unit". They are interconnected by a cabled network (e.g. a four-conductor line), perform the analog to digital conversion of analog signals coming from the groups of sensors and send the resulting digital seismic data to a central recording system 1 (also referred to using the term "central data processing unit"), via intermediate collection devices 2 (also referred to using the term "concentrator device"). The central recording system 1 is usually onboard a recording truck.

The acquisition devices 3 also performs other functions, notably: synchronisation with the central recording system 1, processing of the seismic signal and interfacing with the digital network (i.e. transferring seismic data to the central recording system 1, receiving and processing commands received from the central recording system 1).

FIG. 2 illustrates schematically a seismic data acquisition system according to a second known solution, based on the use of digital sensors 20. Identical elements are designated by the same numerical reference sign.

The digital sensors 20 are generally referred to using the term "Digital Unit". Each digital unit includes a sensor which is a micro-machined accelerometer (also referred to using the term "MEMS-based accelerometer", MEMS being the acronym for "Micro-Electro-Mechanical System").

Comparing with FIG. 1, each digital unit replaces an acquisition device 3 and the string or strings of analog sensors 4 connected to it via a two-conductor line 5. As the acquisition devices 3 of FIG. 1, the digital units 20 are interconnected by a cabled network (e.g. a four-conductor line) and send the digital seismic data to a central recording system 1, via intermediate collection devices 2.

In a known alternative embodiment, the acquisition device 3 or the digital sensors 20 use a wireless network to communicate with the intermediate collection devices 2 and/or the central recording system 1.

In another known alternative embodiment, the acquisition device 3 or the digital sensors 20 use have a memory sufficient for a later seismic data harvesting.

The digital sensors offer advantages over analog sensors (particularly in terms of bandwidth and sensibility stability). However, the solution of FIG. 2 is not optimal if one wishes to make an acquisition with a high number of digital sensors (for example a thousand digital sensors, and thus a thousand digital units 20) by acquisition line (each acquisition line being connected to an intermediate collection devices 2). Indeed, in this case, all the digital units of an acquisition line are connected in series, which requires a robustness of each digital unit (in quality design and manufacturing, which is expensive).

In addition, the solution of FIG. 2 can not have multiple sensor strings on a same acquisition line (as opposed to the solution of FIG. 1, with strings of analog geophones). However, there are advantages to use a string of digital geophones, notably:

a higher fidelity and stability of measures; and
a string of analog geophones gives a measure which is the average of the measurements, while a string of N digital

3

geophones gives N measures. The access to these N measures allows for a spatial filter (combination of the N measures) more effective.

At least for these reasons, and for cost reasons, the inventors have come to the conclusion that it would be interesting to implement strings of digital sensors, using the well-proven string technique comprising a two-conductor line **5** (i.e. the cable of the string) and associated connectors **63**, **64**, casing **61** and housing **62** (see FIG. 6).

In a particular embodiment, the proposed solution should also allow to connect only one digital sensor to the acquisition device, via a two-conductor line (i.e. a connection according to a bus topology, also referred to as a branch topology).

Unfortunately, there is currently no solution as discussed above, combining the concepts of string and digital sensor. In other words, it is not possible today to use the existing cable (two-conductor line **5**) of the strings by replacing, in mechanical housing (or cartridge) **62** of analog sensors, these analog sensors by digital sensors.

As explained above, this mechanical housing **62** of analog sensors is inserted with mechanical tolerances inside a casing **61** (made of plastic in general) which shape is dependent of the type of area to investigate (marsh, land . . .).

It must be noted that it is was not obvious for the Man skilled in the art, at the time the present invention was made, to find a technical solution allowing to connect one or several digital sensors and an acquisition device together, using a two-conductor line usually used to connect one or several analog sensors, which further is a poor quality cable.

Indeed, currently, a given digital unit is connected to another digital unit, via a two pairs line cable, which is a good quality (and therefore expensive) cable, by which:

- the given digital unit receives electrical power (from midpoint of both pairs), used in particular by the digital sensor (MEMS-based accelerometer) included in the given digital unit;

- the two pairs are connected to digital unit and are used in full-duplex mode: one pair as input for commands and synchronization (sampling clock); one pair as output for seismic data obtained by the digital sensor (MEMS-based accelerometer for example) included in the given digital unit (these seismic data being intended ultimately to the central recording system 1);

- the given digital unit receives a sampling clock (common to all digital sensors included in the various digital units), used by the digital sensor (MEMS-based accelerometer) included in the given digital unit. The original sampling clock is a high-precision clock (such as a quartz oscillator) contained in the central recording system 1. The digital units are frequency-dependent by means of a low phase noise analog phase lock loop (PLL) that extracts the sampling clock from the data. Low phase noise is mandatory to achieve accelerometer ultra low noise specifications. To achieve low phase noise with such a PLL, it is mandatory to always have data on the input pair hence to be in full duplex mode. Indeed, periods with no data fed into PLL would lead to PLL drift and phase noise. That's the reason why using such a PLL imposes the use of two pairs.

- the given digital unit sends at least quality control data or seismic data obtained by the digital sensor (MEMS-based accelerometer) included in the given digital unit (these seismic data or quality control data being intended ultimately to the central recording system 1);

- the given digital unit receives command data (that the central recording system 1 sends to the given digital unit).

4

It is important to note that the Man skilled in the art is faced with the following dilemma: the digital sensor should receive the sampling clock via a poor quality (low cost) standard geophone cable (i.e a two-conductor line). But in the facts, this is impossible. Indeed, the noise on the line does not allow it. More precisely, the phase jitter of the data, and hence of the sampling clock after recovery by the PLL, on this cable would be too high. In other words, attenuation and distortion induced by such a cable degrade the temporal precision of a clock that would be transmitted on that cable. Moreover such a cable gets ("picks up") the ambient electronic noise that would degrade even more this clock.

The Man skilled in the art is faced with another problem: the use of a phase lock loop (PLL), in order to recover the sampling clock coming from the central recording system 1, induces a size which is not compatible with a desired objective of placing the digital sensor in a housing containing usually an analog sensor.

The use of a PLL also induces a great power consumption which is not compatible with a desired objective to reduce the power consumption of the acquisition device.

Thus, the Man skilled in the art has therefore no incentive to pursue the course of trying to combine the concepts of sensor string and digital sensors.

6. SUMMARY OF THE INVENTION

A particular embodiment of the invention proposes a seismic sensor adapted to be connected, via a two-conductor line, to an acquisition device which acquires seismic data provided by the seismic sensor and transmits said seismic data to a central recording system or to an intermediate collection device. Said seismic sensor is a digital seismic sensor comprising:

- a digital sensing mean providing said seismic data;
- a local sampling clock providing a sampling frequency to said digital sensing mean;
- means for receiving, via said two-conductor line, synchronization information providing accurate timing information to enable seismic sensor synchronization;
- means for compensating, as a function of said synchronization information, a drift of said local sampling clock;
- means for receiving command data coming from the acquisition device, via said two-conductor line;
- means for transmitting seismic data towards the acquisition device, via said two-conductor line;
- means for driving said means for receiving command data and said means for transmitting seismic data, according to a half-duplex transmission protocol over said two-conductor line and using a transmission clock extracted from the received command data;
- means for receiving electrical power over said two-conductor line; and
- means for coupling said means for receiving command data, said means for transmitting seismic data and said means for receiving electrical power to said two-conductor line.

This particular embodiment relies on a wholly novel and inventive approach, combining several features:

- multiplexing seismic data transmission, command data reception and electrical power on a same two-conductor line. This allows the use of a two-conductor line between the digital seismic sensor and the acquisition device; and
- using a local sampling clock, whose drift is compensated as a function of received synchronization information. In other words, we get rid of high performance PLL that extracts the sampling clock from data. Indeed we sepa-

5

rate (distinguish) the sampling clock, used by the digital sensing mean (e.g. MEMS-based accelerometer), and the transmission clock, used by the means for receiving and transmitting. This allows to use standard low cost/low quality geophone cable (not being designed to transmit high frequency digital data but low frequency analog data), and to use half-duplex mode on a single pair cable.

According to a particular feature, said means for coupling are suitable for a connection to said two-conductor line according to a bus topology.

Thus several digital seismic sensors according to an embodiment of the invention can be connected to a same two-conductor line according to a bus topology (i.e. a branch topology). In other words, this allows to implement a string of digital seismic sensors, which is connected to the two-conductor line. See above the advantages to use such a string of digital sensors.

According to a particular feature, said digital sensing mean comprises an analog/digital converter sampling data at the sampling frequency provided by said local sampling clock, thus providing a series of sampled and dated seismic data having a time provided by the local sampling clock. Said means for compensating a drift of said local sampling clock comprise:

- a time-keeping module for measuring a frequency error of the local sampling clock, the time-keeping module being able to gauge the local sampling clock to the synchronization information; and
- a re-sampling module for correcting the sampled and dated seismic data, as a function of the measured frequency error.

According to a particular feature, the seismic sensor is integrated in a "standard analog geophone" like mechanical housing.

This allows to use a well-proven mechanical housing technique, for the digital sensor.

According to a particular feature, the seismic sensor comprises a all digital delay locked loop, extracting the transmission clock from the received command data.

An ADDLL has the same function as PLL but produces a clock with a high phase noise not compatible with the MEMS-based accelerometer noise specifications.

The fact that, according to the particular embodiment of the invention, the local sampling clock and the transmission clock are dissociated allows to use an ADDLL which has many advantages:

- the ADDLL allows to save board space (it has almost a zero size) and is cheaper (it has almost a zero cost) because it is integrated in a CPU included in the digital sensor (this is hardly the case with a PLL);
- there is no learning phase. In the case of a full-duplex transmission on a two-conductor line, the use of a PLL is acceptable because there is a learning phase (which may take of the order of 200 ms) only once at power up. With a two-conductor line used in half-duplex, the learning phase would take place in each message (at each reversal of direction of communication), which would encumber the bandwidth.

According to a particular feature, said digital sensing mean belongs to the group comprising: MEMS-based accelerometers, MEMS-based velocimeters and analog geophones integrated with an analog/digital converter.

According to a particular feature, said digital sensing mean is a single component sensor.

Thus the compactness of the seismic digital sensor is further improved.

6

According to an alternative embodiment, said digital sensing mean is a three-component sensor.

Another particular embodiment of the invention proposes a string of seismic sensors comprising a plurality of seismic sensors connected to a two-conductor line, said two-conductor line being adapted to be connected to an acquisition device which acquires seismic data provided by said plurality of seismic sensors and transmits said seismic data to a central recording system or to an intermediate collection device. Each of said seismic sensor is a seismic sensor according to any one of the above embodiments.

Another particular embodiment of the invention proposes an acquisition device adapted to be connected, via a two-conductor line, to at least one seismic sensor, said acquisition device comprising means for acquiring seismic data provided by the at least one seismic sensor and means for transmitting said seismic data to a central recording system or to an intermediate collection device. The acquisition device comprises:

- means for transmitting, via said two-conductor line, synchronization information providing accurate timing information to enable seismic sensor synchronization.
- means for transmitting, via said two-conductor line, command data towards the at least one digital seismic sensor;
- means for receiving, via said two-conductor line, digital seismic data coming from said at least one digital seismic sensor;
- means for driving said means for receiving digital seismic data and said means for transmitting command data, according to a half-duplex transmission protocol over said two-conductor line and using a transmission clock obtained from a local clock and said seismic data;
- means for transmitting electrical power over said two-conductor line; and
- means for coupling said means for receiving digital seismic data, said means for transmitting command data and said means for transmitting electrical power to said two-conductor line.

The proposed acquisition device is adapted to cooperate (via a two-conductor line) with one or a string of several new digital seismic sensors as presented above.

According to a particular feature, the acquisition device comprises an analog/digital converter, switching means and means for enabling/disabling such that, if said at least one seismic sensor is of a digital type:

- said switching means directs digital seismic data coming from the at least one seismic sensor towards the means for coupling;
- said analog/digital converter is disabled;
- said means for transmitting command are enabled;
- said means for driving are enabled for driving said means for receiving seismic data and said means for transmitting command data, according to said half-duplex transmission protocol over said two-conductor line and using said transmission clock;
- said means for transmitting electrical power are enabled.

According to a particular feature, said means for enabling/disabling are such that, if said at least one seismic sensor is of an analog type:

- said switching means direct the analog seismic data coming from the at least one seismic sensor towards the analog/digital converter;
- said analog/digital converter is enabled, to convert the analog seismic data into digital seismic data;
- said means for transmitting command are disabled;
- said means for transmitting electrical power are disabled.

Thus, the same acquisition device can be used either with one or a string of several classical analog seismic sensors, or with one or a string of several new digital seismic sensors.

According to a particular feature, the acquisition device comprises means for automatically detecting whether at least one seismic sensor has been connected to the acquisition device via the two-conductor line, and means for automatically determine the type, analog or digital, of the at least one seismic sensor connected to the acquisition device.

According to a particular feature, the acquisition device is integrated in a "standard acquisition device dedicated to analog sensors" like mechanical housing comprising a connector with two contacts, adapted to cooperate with a connector of the same type placed on the two-conductor line.

This allows to use a well-proven mechanical housing technique, for the acquisition device.

According to a particular feature, the acquisition device comprises a all digital delay locked loop, extracting a transmission clock from the received seismic data.

The advantages of using a ADDLL in the acquisition device are the same as those already discussed above for the digital seismic sensor.

7. LIST OF FIGURES

Other features and advantages of embodiments of the invention shall appear from the following description, given by way of indicative and non-exhaustive examples and from the appended drawings, of which:

FIG. 1 illustrates schematically a seismic data acquisition system according to a first known solution, based on the use of analog sensors;

FIG. 2 illustrates schematically a seismic data acquisition system according to a second known solution, based on the use of digital sensors;

FIG. 3 illustrates schematically a seismic data acquisition system according to a particular embodiment of the invention;

FIG. 4 details a string appearing in FIG. 3 and comprising two digital sensors connected to an acquisition device;

FIG. 5 details an alternative embodiment of the acquisition device, to which can be connected either a string of digital sensors, or a string of analog sensors;

FIG. 6 illustrates schematically a string of analog geophones according to the prior art.

8. DETAILED DESCRIPTION

In all of the figures of the present document, identical elements and steps are designated by the same numerical reference sign.

FIGS. 1, 2 and 6 relate to prior art and have already been discussed above.

Referring now to FIG. 3, we present a seismic data acquisition system according to a particular embodiment of the invention.

As in FIGS. 1 and 2, the seismic data acquisition system comprises a central recording system 1 (usually onboard a recording truck) and intermediate collection devices 2 ("concentrator devices").

As in FIG. 1, the system also comprises strings of sensors, but in FIG. 3 the sensors used are new digital sensors 31 (see description of FIG. 4 below).

For a best understanding of the present invention, each reference 31 designates a digital sensor and its corresponding housing and casing.

Each string of digital sensors 31 is connected, via a two-conductor line 5, to a new acquisition device 30, also referred to below as "new Digitizer Unit" (see description of FIG. 4 below).

In this particular embodiment, we have the following features:

- the string cable is a standard low cost/low quality two-conductor line 5 usually used to connect one or several analog geophones, as in FIG. 1, and not being designed to transmit high frequency digital data but low frequency analog data;

- the casing and the housing of each digital sensors 31 are identical to the casing 61 and the housing 62 of an analog sensor 4 (as illustrated in FIG. 6);

- the housing of each acquisition device 30 is identical to the housing of an acquisition device 3 in FIG. 1.

Thus, we use the well-proven string technique comprising a two-conductor line 5 (i.e. the cable of the string) and associated connectors.

In an alternative embodiment, at least one string comprises only one digital sensor 31 connected to an acquisition device 30 via a two-conductor line 5.

FIG. 4 details a string appearing in FIG. 3 and comprising two digital sensors 31 connected to an acquisition device 30, via a two-conductor line 5.

To simplify the FIG. 4, only one acquisition device 30 is shown, and the network between this acquisition device 30 and the central recording system 1 is not shown.

We detail now the structure of a digital sensor 31 and the structure of an acquisition device 30 according to this first embodiment.

The digital sensor 31 comprises:

- a MEMS-based accelerometer 311 (or any other digital sensing mean, e.g. a MEMS-based velocimeter or an analog geophone integrated with an analog/digital converter) providing seismic data. In a particular embodiment, the digital sensing mean 311 is a single component sensor. In an alternative embodiment, the digital sensing mean 311 is a three-component sensor;

- a local sampling clock 317 providing a sampling frequency to the MEMS-based accelerometer;

- a processor 312 (or CPU, for "Central Processing Unit"), and the associated memories (not shown: read-only memory (ROM) and random access memory (RAM));

- a receiving block 315 for receiving the electrical power over the two-conductor line 5;

- a receiving block 313 for receiving:
 - command data coming from the acquisition device 30; and

- synchronization information (like synchronization frame) providing accurate timing information to enable sensor synchronization, said synchronization information being received from the acquisition device 30 via said two-conductor line 5;

- a transmitting block 314 for transmitting at least the seismic data and quality control data towards the acquisition device 30;

- a ADDLL block 316 ("all digital delay locked loop"), extracting a transmission clock from the received command data coming from the acquisition device 30;

- a coupling block 318 for coupling the receiving blocks 313 and 315 and the transmitting block 314 to the two-conductor line 5. In a particular embodiment, the coupling block 318 is suitable for a connection to the two-conductor line 5 according to a bus topology.

The coupling block 318 allows to multiple seismic data transmission, command data reception and electrical power

on the same two-conductor line **5**. In more details, the coupling block **318** comprises: a first capacitor (connected between a first conductor of the two-conductor line **5** and a first input of the receiving block **313** and a first output of the transmitting block **314**), a second capacitor (connected between a second conductor of the two-conductor line **5** and a second input of the receiving block **313** and a second output of the transmitting block **314**), a first inductor (connected between the first conductor of the two-conductor line **5** and a first input of the receiving block **315**) and a second inductor (connected between the second conductor of the two-conductor line **5** and a second input of the receiving block **315**).

The processor **312** implements the following additional functions:

- compensating a drift of the local sampling clock **317**; and
- driving the receiving block **313** and the transmitting block **314**, according to a half-duplex transmission protocol over the two-conductor line **5** and using the transmission clock extracted by the ADDLL block **316** from the received command data coming from the acquisition device **30**.

In a particular embodiment, the function of drift compensation is adapted from the teaching of the U.S. Pat. No. 7,548,600 B2 in the name of Serceel.

In the present context, the MEMS-based accelerometer **311** comprises an analog/digital converter (not shown) sampling data at the sampling frequency provided by the local sampling clock **317**, thus providing a series of sampled and dated seismic data having a time provided by the local sampling clock **317**.

The processor **312**, or a coprocessor (not shown), comprises:

- a time-keeping module, for measuring the frequency error of the local sampling clock **317**, the time-keeping module being able to gauge the local sampling clock to the synchronization frames (also called “reference clock”); and
- a re-sampling module, for correcting the sampled and dated seismic data, as a function of a measured frequency error.

All implementation details relating to the time-keeping module and the re-sampling module can be found in the description and drawings of the aforesaid U.S. Pat. No. 7,548,600 B2.

In a particular embodiment, the function of driving the receiving block **313** and the transmitting block **314**, according to a half-duplex transmission protocol over the two-conductor line **5**, is carried with any known half-duplex transmission protocol.

In an alternative embodiment, all the functions implemented by the processor **312** (see above discussions) can be implemented by a dedicated hardware machine or component such as an FPGA (Field Programmable Gate Array), an ASIC (Application-Specific Integrated Circuit) or any other hardware module (or any combination of several hardware modules). Any form combining a hardware portion and a software portion can also be used.

In the first embodiment illustrated in FIG. 4, the acquisition device **30** comprises:

- a local clock **307**;
- an interfacing block **301** for interfacing with the network (i.e. an intermediate collection devices **2** (“concentrator devices”) or another acquisition device);
- a processor **302** (or CPU, for “Central Processing Unit”), and the associated memories (not shown: read-only memory (ROM) and random access memory (RAM));

- a power supply block **305** for supplying the electrical power to the digital sensors **31**, over the two-conductor line **5**;

- a receiving block **303** for receiving at least seismic data and quality control data coming from the digital sensors **31**;

- a transmitting block **304** for transmitting:

- the command data towards the digital sensors **31**; and
- synchronization information (like synchronization frame) providing accurate timing information to enable sensor synchronization, said synchronization information being transmitted via the two-conductor line **5**;

- a ADDLL block **306** (“all digital delay locked loop”), extracting a transmission clock from the received bits contained in the data sent to the acquisition device **30** by the digital sensors **31**;

- a coupling block **308** (identical to coupling block **318** included in the digital sensor **31**) for coupling the receiving blocks **303** and **305** and the transmitting block **304** to the two-conductor line **5**. In a particular embodiment, the coupling block **308** is suitable for a connection to the two-conductor line **5** according to a bus topology.

The processor **302** implements the traditional functions of an acquisition device (e.g. processing of the seismic signal and interfacing with the digital network, i.e. transferring seismic data to the central recording system **1**, receiving and processing commands received from the central recording system **1**, etc).

The processor **302** also implements the following additional function: driving the receiving block **303** and the transmitting block **304**, according to a half-duplex transmission protocol over the two-conductor line **5** and using the transmission clock extracted by the ADDLL block **306** (see above).

In a particular embodiment, the function of driving the receiving block **303** and the transmitting block **304**, according to a half-duplex transmission protocol over the two-conductor line **5**, is carried with any known half-duplex transmission protocol. If the string comprises several digital sensors **31** (case shown in FIG. 4), the half-duplex transmission protocol also manages a time division multiplexing (TDM) so as to share the access to the two-conductor line **5** among the acquisition device **30** and the digital sensors **31**.

In an alternative embodiment, all the functions implemented by the processor **302** (see above discussions) can be implemented by a dedicated hardware machine or component such as an FPGA, an ASIC or any other hardware module (or any combination of several hardware modules). Any form combining a hardware portion and a software portion can also be used.

FIG. 5 details an alternative embodiment of the acquisition device **50**, to which can be connected either a string of digital sensors (not shown in FIG. 5, but identical to the case shown in FIG. 4), or a string of analog sensors **51** (case shown in FIG. 5).

Analog sensors **51** are well-known and not described in detail here.

In this alternative embodiment, the acquisition device **50** differs from the one **30** of FIG. 4 in that it comprises an analog/digital converter **309** and a switching block **310** which selects the path for the data coming from the sensors, depending on the type of these sensors (analog or digital), and in that the processor **302** also implements an enabling/disabling function such that:

- if the seismic sensors are of the analog type (case shown in FIG. 5):

11

the switching block **310** directs the data (coming from the analog sensors **51**) towards the analog/digital converter **309**;

the analog/digital converter **309** is enabled, to convert the seismic data provided by the analog sensors **51** into digital seismic data;

the transmitting block **304** is disabled;

the power supply block **305** is disabled;

if the seismic sensors are of the digital type (case shown in FIG. **4**):

the switching block **310** directs the data (coming from the digital sensors **31**) towards the coupling block **308**;

the analog/digital converter **309** is disabled;

the transmitting block **304** is enabled;

the driving function is enabled for driving the receiving block **303** and the transmitting block **304**, according to a half-duplex transmission protocol over the two-conductor line **5** and using the transmission clock;

the power supply block **305** is enabled.

In a particular embodiment, the acquisition device **50** comprises means for automatically detecting whether a seismic sensor or a string of seismic sensors has been connected to the acquisition device via the two-conductor line, and means for automatically determine the type, analog or digital, of the seismic sensor or seismic sensors connected to the acquisition device.

At least one embodiment of the disclosure provides a technique for connecting one or several seismic digital sensors with an acquisition device, using a two-conductor line usually used to connect one or several seismic analog sensors, even though this two-conductor line is a poor quality cable.

An embodiment provides a technique of this kind allowing to use the existing cable (two-conductor line) of the known strings, by replacing, in mechanical housing of analog sensors, these analog sensors by digital sensors. Thus, the cost of development of such a technique is low (no new mechanical housing development is necessary).

In other words, at least one embodiment provides a digital sensor having a compactness which allows to place it in a housing containing usually an analog sensor.

An embodiment provides a technique of this kind allowing to reduce the power consumption of the acquisition device.

The invention claimed is:

1. A seismic sensor adapted to be connected, via a two-conductor line, to an acquisition device which acquires seismic data provided by the seismic sensor and transmits said seismic data to a central recording system or to an intermediate collection device, wherein said seismic sensor is a digital seismic sensor comprising:

a digital sensor providing said seismic data;

a local sampling clock providing a sampling frequency to said digital sensor;

a data and information receiver configured to receive, via said two-conductor line, synchronization information providing timing information to enable seismic sensor synchronization, and to receive command data coming from the acquisition device, via said two-conductor line;

a transmitter configured to transmit seismic data towards the acquisition device, via said two-conductor line;

a processor configured to compensate, as a function of said synchronization information, a drift of said local sampling clock, and to drive the data and information receiver to receive the command data and the synchronization information, and to drive the transmitter to transmit the seismic data, according to a half-duplex

12

transmission protocol over said two-conductor line and using a transmission clock extracted from the received command data;

a power receiver coupled to receive electrical power over said two-conductor line; and

a coupling circuit, which couples the data and information receiver, the transmitter and the power receiver to said two-conductor line.

2. The seismic sensor according to claim **1**, wherein said coupling circuit is suitable for a connection to said two-conductor line according to a bus topology.

3. The seismic sensor according to claim **1**, wherein said digital sensor comprises an analog/digital converter sampling data at the sampling frequency provided by said local sampling clock, thus providing a series of sampled and dated seismic data having a time provided by the local sampling clock,

and wherein, in view of compensating said drift of said local sampling clock, the processor comprises:

a time-keeping module for measuring a frequency error of the local sampling clock, the time-keeping module being able to gauge the local sampling clock to the synchronization information; and

a re-sampling module for correcting the sampled and dated seismic data, as a function of the measured frequency error.

4. The seismic sensor according to claim **1**, further comprising a all digital delay locked loop, extracting the transmission clock from the received command data.

5. The seismic sensor according to claim **1**, wherein said digital sensor belongs to the group consisting of: MEMS-based accelerometers, MEMS-based velocimeters and analog geophones integrated with an analog/digital converter.

6. The seismic sensor according to claim **1**, wherein said digital sensor is a single component sensor.

7. The seismic sensor according to claim **1**, wherein said digital sensor is a three-component sensor.

8. A system comprising a string of a plurality of seismic sensors according to claim **1**, which are connected to the two-conductor line, said two-conductor line being adapted to be connected to the acquisition device which acquires the seismic data provided by said plurality of seismic sensors and transmits said seismic data to the central recording system or to the intermediate collection device.

9. An acquisition device adapted to be connected, via a two-conductor line, to at least one seismic sensor and to acquire seismic data provided by the at least one seismic sensor, said acquisition device comprising:

a seismic data transmitter configured to transmit said seismic data to a central recording system or to an intermediate collection device;

a local clock

a data and information transmitter configured to transmit, via said two-conductor line, synchronization information providing timing information to enable seismic sensor synchronization, and to transmit command data towards the at least one digital seismic sensor;

a receiver configured to receive, via said two-conductor line, digital seismic data coming from said at least one digital seismic sensor;

a processor configured to drive said receiver and said transmitter, according to a half-duplex transmission protocol over said two-conductor line and using a transmission clock obtained from said local clock and said seismic data;

a power transmitter configured to transmit electrical power over said two-conductor line; and

13

a coupling circuit, which couples the receiver, the data and information transmitter and the power transmitter to said two-conductor line.

10. The acquisition device according to claim **9**, further comprising an analog/digital converter, a switching block and, wherein the processor is configured to implement an enabling/disabling function such that, if said at least one seismic sensor is of a digital type:

said switching block directs digital seismic data coming from the at least one seismic sensor towards the coupling circuit;

said analog/digital converter is disabled;

said data and information transmitter is enabled;

said processor is enabled for driving said receiver and said data and information transmitter, according to said half-duplex transmission protocol over said two-conductor line and using said transmission clock;

said power transmitter is enabled.

14

11. The acquisition device according to claim **10**, wherein said enabling/disabling function implemented by the processor is such that, if said at least one seismic sensor is of an analog type:

said switching block directs the analog seismic data coming from the at least one seismic sensor towards the analog/digital converter;

said analog/digital converter is enabled, to convert the analog seismic data into digital seismic data;

said data and information transmitter is disabled;

said power transmitter is disabled.

12. The acquisition device according to claim **10**, wherein the processor is configured further to automatically detect whether at least one seismic sensor has been connected to the acquisition device via the two-conductor line, and to automatically determine the type, analog or digital, of the at least one seismic sensor connected to the acquisition device.

13. The acquisition device according to claim **9**, further comprising a all digital delay locked loop, extracting a transmission clock from the received seismic data.

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